

**REMARKS**

The Office Action of June 6, 2005 has been carefully considered. In response thereto, the claims have been amended as set forth above. Reconsideration and allowance in view of the foregoing amendments and the following remarks is respectfully requested.

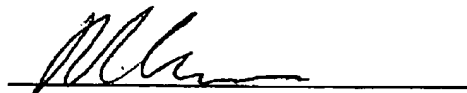
Claims 1-3 and 5-7 were rejected as being anticipated by Walters. The claims have been amended to more clearly distinguish over the cited references. Reconsideration is respectfully requested.

In particular, the claim 1 has been amended to recite that the Virtual Machine Interpreter comprises a *hardware* pre-processor including a program counter, separate from the processor core. Walters is not believed to teach or suggest such a feature. Rather, Walters teaches a general software cross-compilation method that is orders of magnitude slower in its execution than the present invention.

Claim 5 has likewise been amended to recite the use of a hardware pre-processor.

Withdrawal of the rejection and allowance of claims 1-7 is respectfully requested.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'M. Ure', is written over a horizontal line.

Michael J. Ure, Reg. 33,089

Dated: January 5, 2006